· FPGA Design Experience in development of modules related to RF Front End (DDC/DUC/DPD/CFR) etc.

· Develop, implement, validate and verification of DSP algorithms in MATLAB related to MIMO/Beamforming.

· Experience in LTE or any OFDM based PHY module development.

· Performance verification and validation of developed modules in FPGA based prototypes and integrate with baseband and RF subsystems

· 8-12 years of RTL Design and Development using VHDL/Verilog.

· Participate in Baseband-RF interface bring-up of radio boards

· Working experience with Test instruments (Signal Generator, Signal Analyzer and Logic Analyzer) for System and module testing.

· Work closely with RF board design and firmware teams for FPGA integration and testing

· Support pre and post sales activities.